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10/072,145

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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OFFICIAL

In re Application of: Averett, Guy E. et al.
Serial No.: 10/072,145
Filing Date: 02/07/2002
Group Art Unit: 2811
Examiner: Magee, Thomas J.
Title: SEMICONDUCTOR DEVICE AND METHOD OF
PROVIDING REGIONS OF LOW SUBSTRATE
CAPACITANCE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE
TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE, ATTN: EXAMINER
Thomas J. Magee AT THE FAX NO. (703) 872-9306

ON: January 15, 2004
Date of Facsimile Transmission
Lydia McNamara 12 pages
Name of Person Signing Certificate
Lydia McNamara 1/15/04
SIGNATURE DATE

Honorable Commissioner of Patents,
P.O. BOX 1450
Alexandria, VA 22313-1450

RULE 116 AFTER FINAL AMENDMENT

SIR:

In response to the office action mailed October 15,
2003, please amend the above-identified application as
follows.